**USB-Blaster™ Programming Cable**

The USB-Blaster download interface connects to a standard USB PC port. The cable is a hardware interface to either a standard PC or a Linux/Unix workstation and provides both static and dynamic IP addressing. It can be plugged into an existing 10/100 Base-T Ethernet network to communicate with remote devices or processors directly or indirectly via a standard 10/100 Base-T Ethernet port using a crossover cable. Because design changes are downloaded directly to the device, prototyping is easy and you can accomplish multiple design iterations in quick succession. Harnessing the power of an Ethernet network, multiple users can remotely access Altera devices, bringing a level of productivity to development and debugging.

- **Supported Devices:** Stratix series FPGAs, Cyclone series FPGAs, MAX V series FPGAs, Stratix III series FPGAs, Stratix IV series FPGAs, Stratix V series FPGAs, Cyclone II series FPGAs, Cyclone III series FPGAs, Cyclone IV series FPGAs, Cyclone V series FPGAs, Stratix GX series FPGAs, APEX 3 series FPGAs, APEX 20K series FPGAs, and APEX 70K series FPGAs.
- **Configuration:** MAX II CPLD and flash fast passive parallel configuration.
- **On-board USB-Blaster:**
- **Cables and Power:**

**Ordering Information**

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<th>Price</th>
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<tr>
<td>544-1755-ND (PL-USB-BLASTER-RCA)</td>
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**ByteBlaster™ Programming Cable**

The ByteBlaster cable can program and configure 2.5V, 3.3V, 3.0V and 5.0V devices. The cable also supports active serial programming of serial configuration devices. The core device configuration data ranges from 32Kbit to 16Mbit, with many different types available at a variety of speeds. And it can be used to program 2558.77

**Ordering Information**

<table>
<thead>
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<tr>
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Cyclone® III FPGA Development Kit

Cyclone® III FPGA Development Kit combines the largest density-low cost, low-power FPGA available with a robust set of development tools and includes a free portfolio of IP cores. The kit facilitates the design and verification portion of your project, whether it’s for automotive, consumer, wireless communications, video processing, or another high-volume, cost-sensitive application.

Kit Includes:
- Cyclone® III Development Board:
  - Cyclone® III EP3C25F324 FPGA
  - Embedded USB-Blaster™ circuitry
  - 20-pin JTAG/ADP connector
  - Flash memory
  - Cyclone® III EP3C120F780 FPGA
  - Cyclone® III EP3C25F324 FPGA
- Cyclone® III Development Kit CD-ROM:
  - Nios® II Embedded Design Suite Evaluation (no charge)
  - Design examples
  - Connectors:
    - Two HSMCs
    - USB type B
- Cyclone® III EP3C25F324C1N device

Includes:
• Subscription Edition (optional, available for purchase)
• Board schematic and layout
• Bill of materials
• Product and partner information
• Quartus® II software design files

Cyclone® III LS FPGA Development Kit

Altera’s Cyclone® III LS FPGA Development Kit combines the largest density-low power FPGA available with a complete suite of features implemented in the silicon, software, and intellectual property (IP) levels. These security features provide passive and active protection of your IP (from pre-silicon through engineering, manufacturing, and customer use).

Kit Contents:
- Cyclone® III LS FPGA Development Board:
  - Cyclone® III LS FPGA Evaluation Kit, CD ROM:
    - High-performance Stratix® IV GX FPGA (with JTAG bypass and Embedded USB-Blaster™ circuitry)
- Cyclone® III Development Kit CD-ROM:
  - Nios® II Embedded Design Suite Evaluation (no charge)
  - Design examples
  - Connectors:
    - Three HSMC debug cards (two loop-back and a debug header)

Embedded Systems Cyclone® III FPGA Development Kit

The Embedded Systems Cyclone® III FPGA Development Kit is a complete development platform for prototyping embedded systems on Altera’s low-cost, low-power FPGA family.

Kit Contents:
- Cyclone® III Development Board:
  - Cyclone® III EP3C25F324 FPGA
- Cyclone® III Development Kit CD-ROM:
  - Nios® II Embedded Design Suite Evaluation (no charge)
  - Design examples

Nios® II Embedded Evaluation Kit, Cyclone® III Edition

The Nios® II Embedded Evaluation Kit, Cyclone® III Edition is a first-class low-cost evaluation platform for embedded developers. Software developers, especially those new to FPGA design, can install and evaluate the Nios® II Embedded Design Suite (EDS) - a comprehensive software development suite for embedded applications - at no cost.

Kit Includes:
- Cyclone® III Evaluation Board:
  - Cyclone® III EP3C25F324 FPGA
- Cyclone® III Development Kit CD-ROM:
  - Nios® II Embedded Design Suite Evaluation (no charge)
  - Design examples

Cyclone® IV GX Transceiver Starter Kit

The Cyclone IV GX Transceiver Starter Kit features the following: Cyclone IV GX Transceiver Starter Board • Cyclone IV GX EP4SGX230N FPGA • Configuration status and set-up elements • JTAG bypass to access Stratix® IV GX Embedded Evaluation Kit, Cyclone® III Edition (includes both the hardware and software needed to immediately begin developing FPGA designs. With this PCI-SIG compliant board and a one-year license for the Quartus® II design software, you can: 

Stratix® III FPGA Development Kit

The Stratix® III FPGA Development Kit delivers a complete environment for the development and testing of designs requiring high-performance and high-density devices. Altera Stratix® III FPGAs combine the worlds highest performance and highest density with the lowest possible power consumption. You'll find Stratix III FPGAs provide the world's highest performance Stratix® device family, and the next generation of high-performance Stratix® devices with high integration capabilities, affordability, high performance, and advanced imaging equipment. Kit Includes: Stratix® III EP3S15F1200 High-Performance FPGA: • 145.500 equivalent logic elements (LEs): 744 user I/O pins: 384 user I/O pins: • 125MHz, 25MHz, 125MHz, 90MHz, and 180MHz oscillator: 125MHz oscillator: 

Stratix® IV FPGA Development Kit

The Stratix IV FPGA Development Kit delivers a complete system design-level environment that includes both the hardware and software needed to immediately begin developing FPGA designs. With this Stratix IV FPGA Development Kit, you will receive:

Cyclone® IV GX FPGA Development Kit

The Cy...
Altera's Transceiver Signal Integrity Development Kit. Stratix IV GX Edition enables a thorough evaluation of transceiver interoperability and serial/analog/digital (S/AD) signal integrity by allowing you to:

- Evaluate transceiver performance up to 40 Gbps on backplane or LT Hybrid cables
- Measure open eye waveform on backplane cables and LT Hybrid cables
- Use pattern generator and analyzers to measure the BER on backplane cables

The Stratix IV GX Development Kit includes:
- Stratix IV GX Development Board (P0001-ND):
  - Stratix IV GX FPGA
  - Cyclone II EP2C20F484C7 FPGA
  - High-speed AD/DA cards
  - 125MHz Oscillator
  - 100MHz, 75MHz, and 28.63MHz oscillator
  - 24-bit CD-quality audio CODEC
  - Four 7-segment displays
  - 10 toggle switches and 4 LED indicators
  - Power supply circuitry, including 3.3V, 5V, and high-speed backplane power
  - 426.03

- USB-Blaster™ Cable

Highspeed AD/DA Cards
Features:
- Dual 4-channel with 14-bit resolution and data rate up to 65MSPS
- Dual D/A channels with 14-bit resolution and data rate up to 125MSPS
- Dual input channels include HDMI and GPIO, which are fully compatible with Cyclone III Starter Kit and DE3/DE4/DE5 respectively
- Clock sources include oscillator 100MHz, SMA for AD and DA each, and PLL from either HDMI or GPIO interface

DE3 Development and Education Board
Specifications:
- Altera Cyclone III EP1C32 FPGA device
- Altera Stratix Signal Configuration device

DE2 Development and Education Board
Specifications:
- Altera Cyclone II EP2C62 FPGA and EP2S62B device
- Stratix II EP2S62B device

DE2-70 Development and Education Board
Specifications:
- Altera Cyclone II EP2C62 FPGA device
- Altera Stratix Signal Configuration device

Max II Micro Package
The Max II Micro package contains all components needed to use the Max II Micro board in conjunction with a computer that runs the Microsoft Windows software.

References:
- USB-Blaster™ Cable

All prices in euro and include duties.